NEWTON TRAINING (2017):

RADIOMETER AND SQUARE LAW DETECTOR

Basics and HartRAO implementations.

Basic Radiometer

- A device for measuring the radiant flux (power) of Electromagnetic radiation (Wiki)
- Dickie in 1946 was one of the forerunners of such an instrument
- He also advocated the close correlation between 'Johnson Noise' in resistors and 'Thermal Radiation of Black Bodies'
- The definition above implies the whole receiver is the radiometer. At HartRAO we tend to consider only the IF amplifier, detector and V/F parts the radiometer.

Useful Formula and Definitions

- Flux Density received is measured in Janskys(Jy)- units are **10^(-26) Watts per square meter per Hz** -Krauss
- The significance of this being that received signals tend to be smaller than the inherent noise of the receiver amplifiers and electronics, and are of noise like appearance which makes measurement difficult.
- Thermal Noise Power generated by a resistor P = kTB where T is temperature(K), B bandwidth in Hz and k is Boltzmans constant 1.38 x 10⁽⁻²³⁾ units are in WHz⁽⁻¹⁾K⁽⁻¹⁾ as an example if a resistor is at room temperature of 290 K then power is -174dBm per 1 hertz of bandwidth. For a 50 ohm resistor this is 0.9 nano volts
- For Amplifiers of gain Gn and Noise Temperature Tn when Cascaded according to Friss:- equivalent noise temperature T total= T1 +T2/G1+T3/G1XG2 +.... and so on -this means that the first stage is *Dominant* and should have the gain as high as possible and its noise temperature should be as low as possible.
- Manufacturers often quote in Noise Figure (F) relative to ambient 290K : F=1 +T/290 and in decibels this is NF= 10log(F) dB
- Noise Voltage has zero mean and varies randomly on very short time scales (nano seconds) comparable to the inverse bandwidth of the radiometer. If this voltage is squared and averaged over long periods(seconds) then a positive steady output is obtained which is proportional to the incoming noise power. By integrating a large number of discrete noise samples allows the average noise power to be determined to a very low fractional uncertainty (<<1). This enables faint sources that change the antenna temperatures by fractional amounts compared to the total noise power to be detected. The device used to do this is the Square law detector diode.

When a radio telescope looks at a radio source in the sky the receiver output is made up of several components, and is known as the System Noise Temperature **Tsys=Tcmb +Tsource+Tatm+Trec+Tspill**

Tcmb= microwave background radiation ~2..73K, Tspill= ground radiation spillover, Tatm =atmospheric effects/water vapour, Trec =noise from amplifiers/mixers etc, Tsource = radiation that we are measuring which can be very small compared to the others(<0.1K)

• The main parts of the Radiometer are thus a BandPass Filter, a Square-Law Detector, an Averager (integrator) and a Voltmeter



Total Power Radiometer (HartRAO Control room)







A method to implement gain stabilisation by using AGC on the IF amp to control overall gain of the receiver. The noise from a Ndiode source is square wave modulated and injected via a coupler into the input LNA. The modulated component is extracted using a synchronous demodulator. This output after filtering is used to generate an AGC control signal.

No longer used on the 26m since the advent of better (more stable) amplifiers.

Figure 6: The 13 cm (2.3 GHz) noise-adding receiver system used at HartRAO.



One beam is on source the other on sky only. The synchronous demodulator Causes the two beams to be subtracted leaving only the source. ie atmospheric noise effects are canceled out.

Figure 7: The Dicke-switched dual-beam receiver system used at HartRAO.

Dickie Switched Systems

Continuous Noise Injection



Control Room Radiometers 26 meter Antenna



MANUAL STEP ATTENUATOR



Radiometer

15 M ANTENNA S/X RADIOMETERS



Microwave Detector Diode

All diodes essentially work in three regions: above 20dBm - Linear, between -20dBm and +20dBm - transition and below -20dBm - the square law region. The later is the region which we require – for converting the incoming signal (power) into some easily measured signal (voltage) -ie if the input power increases by 3dB then the output voltage should double. The basic circuit is as follows:



Resistor R is a wide band Match. (50 ohms)

The diode acts like a half wave rectifier, giving positive outputs in this case. The capacitor C is known as the video capacitor and determines the response time to sudden changes in input conditions (noise calibration switch on/off for example) as well as smoothing(averaging) out the signal -commonly referred to as Envelope detection. The choice of Diode is critical, for a wide bandwidth a fast type is essential (low input capacitance in the diode itself). Material type affects the sensitivity of the circuit (curve sharpness) germanium and schottky are good whereas silicon is poor. Tunnel diodes have proven themselves to be robust efficient square law detectors- the first HartRAO detectors used a BD3 germanium back tunnel diode and the later circuits used MBD1057 planar back tunnel diodes, also germanium. The Demonstration Radiometer boards use zero bias schottky diodes which are not so robust, care has to taken to prevent inadvertent input overload from destroying the diode.

Detectors 1975-2014 and VTF 1995



Detector Diode cont

For the dc amplification stage an op amp circuit is used, with a voltage gain of ~100, choice of component is again critical- a good quality instrumentation type, low noise, low offset voltage,temperature stable and a reasonable bandwidth etc. A general purpose LM741 is not suited! Our choice is the OP177 working with a dual voltage rail. Provision for zeroing out any offset voltage when no signal input is present is needed otherwise the square law response will be compromised, particularly at low input power levels.



By 'loading' the output of the detector diode with a variable resistor enables the linearity curve to be adjusted somewhat. Noting that some diodes are better (squarer) than others. The capacitor across the feedback resistor is used for AC integration of the input signal- (active low pass filter).

With the antenna at Zenith (cold sky) the dc output is adjusted with the step attenuators to be 300mV nominal, allowing sufficient (square law) range on strong signals for the detector to work over. Poor settings will lead to the diode operating in its transition or even the linear region.



FIG. 1.1 Diode characteristic. Note differing forward and reverse scales



Package Electrical Specifications, $T_A = 25$ °C

Model	lp		C _T	γ	R _v	Ip/Iv	VR	VF	
	МIN µ A	МАХ µА	MAX pF	TýP mV / mW	TYP Ω	MIN	MIN m∨	MAX mV	Package
MBD1057-E28 / 28X	100	200	0.40	1,000	180	2.5	420	135	E28 / 28X
MBD1057-H20	100	200	0.50	1,000	180	2.5	420	135	H20
MBD1057-T54	100	200	0.55	1,000	180	2.5	420	135	T54
MBD 1057-T80	100	200	0.65	1,000	180	2.5	420	135	T80
MBD2057-E28 / 28X	200	300	0.40	750	130	2.5	410	130	E28 / 28X
MBD2057-H20 .	200	300	0.50	. 750	130	2.5	410	130	' H20
MBD2057-T54	200	300	0.55	. 750	130	2.5	410	130	T54
MBD2057-T80	200	300	0.65	750	130	2.5	410	130	T80
MBD3057-E28 / 28X	300	400	0.45	500	80	2.5	400	125	E28 / 28X
MBD3057-H20	300	400	0.55	500	80	2.5	400	125	H20
MBD3057-T54	300	400	0.60	500	80	2.5	400	125	T54
MBD3057-T80	300	400	0.70	500	80	2.5	400	125	T80
MBD4057-E28 / 28X	400	500	0.50	275	65	2.5	400	120	E28 / 28X
MBD4057-H20	400	500	0.60	275	65	2.5	400	120	H20
MBD4057-T54	400	500	0.65	275	65	2.5	400	120	T54
MBD4057-T80	400	500	-0.75	275	65	2.5	400	120	T80
MBD5057-E28 / 28X	500	600	0.55	250	60	2.5	400	110	E28 / 28X
MBD5057- H20	500	600	0.65	250	60	2.5	400	110	H20
MBD5057- T54	500	600	0.70	250	60	2.5	400	110	T54
MBD5057- T80	500	600	0.80	250	60	2.5	400	110	T80
Test Conditions			$V_R = V_V$ F= 100 MHz	$P_{IN} = -20 \text{ dBm}$ R _L = 10 KΩ F = 10 GHz			I _R = 500 μA	$I_F = 3 \text{ mA}$	





At low signal levels, all three types of diodes closely obey the equation for the ideal diode, and this will be the starting point for the discussion of detector action. The equation relates the current through the diode to the voltage appearing across the junction. The characteristics of the diode are reflected in the so-called "thermal voltage", Vt and the reverse saturation current Is. At a given temperature Vt will be different for various types of diodes, and this is reflected by the value of the ideality factor n which has a value in the range of 1 to 2. The predominant factor which determines the characteristics of the diode is the value of the reverse saturation current Is. This current is a function of device area, materials used to form the junction, and temperature. The temperature dependence is very important because most of the change in detector performance can be related to variations in Is, which changes by approximately a factor of 2 every 20°C.



One of the major applications of diode detectors is to measure either absolute or relative power levels. At low power levels (<-20 dBm), the diode responds to the square of the voltage appearing across the junction so the detected signal becomes a function of power. An approximate but accurate analysis reveals the reason for the square law response. The diode current can be estimated by a series expansion of the diode equation. Upon substitution of a sinusoidal description for the signal voltage, restricting the analysis to signal levels less than Vt, and assuming that the detected signal is negligible with respect to the input voltage, one finds that three dominant terms appear in the expression. Two of these describe the fundamental and second harmonic of the input voltage represents the detected signal. Experimental evidence on a wide variety of detectors confirms the validity of the analysis. It should not be surprising that significant departure from square law is noted for signal levels exceeding 26 millivolts (about -22 dBm in a 50-ohm system) and the detected output level is about 15% of the peak RF input voltage at this power level.



In the square law region, the waveform of the diode current is almost identical to that of the input signal. Under large signal operation, the diode current waveform becomes a function of input signal level and video output voltage. The output voltage becomes a complicated function of load resistance, power level, and diode characteristics. The key point is that the detection law will undergo a transition from square law to linear. This occurs when the junction voltage exceeds Vt. In the linear mode the diode is acting like the familiar peak detector.



V/F and Counters

A linear conversion from volts to frequency is an alternative method of converting the voltage output from the detector into a digital format for use by a computer. The KAT team are using very fast analogue to digital converters of wide bandwidth, 1 to 2 GHz in order to digitize L band RF directly - ie no down converters (which add noise). HartRAO is using the ic type AD650 to achieve a similar conversion. The output from the Detector is amplified with a X10 gain (OP177) op amp before feeding the AD650s Input pin.

Freq Out 1 MHz Volts Input 10 V

This is done to use the maximum range of the AD650. The chip is clocked with a 5 MHz Xtal oscillator to achieve maximum precision of conversion and very low linearity errors (< 0.2%). and a low 20ppm temperature variation. This TTL digital logic output is then easily carried from the antenna to a 'Counter' unit in the control room via coaxial cable or multimode fiber optic cable. The 15m has splitters in the pedestal to send the RF by coax and the detector signals over fibers. The 26m will be upgraded in due course. Fiber is immune to EMI interference effects , and in digital format is virtually loss-less. Coaxial cables are lossey at high frequencies and need cable compensators to correct for this.

V/F and Counters cont.

As part of the 26m Antenna Control System a Rack (cage) holds up to 16 counter cards to serve all 7 receivers on the 26m and the S/X receiver on the 15m to count the pulses from all the V/F converters. Each card is individually accessed (read/write) via a 24 bit computer bus. (1 MHz requires 20 bits).

In the timing room a network server transmits a time code signal (at TTL base band level) which is received by a decoder card in the cage. This provides a precise 10ms interval period in addition to the encoded time information. All Instruments (synthesizers) are synchronised to the 5 MHz maser (via their external ref input sockets).

The counter cards use simple Cmos logic counter ic's, and are synced using the *Latch* signal, to reset all the counters and start a new count sequence (every 10ms). The bus is linked to a PC104 I/O card in the Control System PC running Linux RTO. Software running on this PC handles additionally the motor controllers on the 26m, and from 19 bit encoder read outs enables the required pointing of the antenna to a source in the sky all in real time.

Useful Test Equipment for the Lab.

Signal Generator



Power meter



Vector Network Analyzer

Spectrum Analyzer







Detector (Input to Output)

Figure 8: A diagram showing the time and frequency domain representations of the predetection signal, post-detection square-law rectified signal, and the integrated radiometer output signal.

